REMARKS

Amendments to the specification merely correct typographical errors. No new matter has been added.

No fee is believed to be due. Please apply any charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: _July 16, 2003_

Faustino A. Lichauco
Reg. No. 41,942

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110-2804
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

20690607.doc

Amendments to the Specification:

Please replace the paragraph beginning at page 3, line 17 with the following amended paragraph:

a1)   at least one frame synchronization word having [[N [sic]]] $\underline{K}$ frame synchronization bits, and

Please replace the paragraph beginning at page 4, line 26 with the following amended paragraph:

In accordance with a further preferred development of the present invention, a frame start of the at least one data frame is defined by a frame synchronization word when the frame synchronization word is preceded by at least [[N [sic] =]] N+K+1 dummy bits.

Please replace the paragraph beginning at page 5, line 9 with the following amended paragraph:

In accordance with yet another preferred development of the present invention, the at least [[N [sic] =]]N+K+1 dummy bits which precede the frame synchronization word are provided as logic ones "1", so that, after a sequence of a number of N+K+1 dummy bits in a synchronization bit detection unit it is possible to ascertain that a frame start is present.